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## OP-S10G54-80 10Gbps SFP+ Bi-Directional Transceiver, TX1550/RX1490 80km Reach

### **Product Features**

- Supports up to 10.7Gbps bit rates
- Hot-pluggable SFP+ footprint
- 1550nm Cooled EML laser and APD photodiode, Up to 80km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with single LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating temperature: 0 to +70°C



- 10Gbps Optical systems
- 10GBASE-ZR at 10.3125Gbps
- 10GBASE-BX Ethernet



# **Description**

OPTINET Bidi SFP+ transceiver is designed for single fiber Bidirectional serial optical data communication up to 60KM on SMF. The transceiver consists of three sections: a Cooled EML laser transmitter, a APD photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements. It is compliant with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.

# **Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

# **Recommended Operating Conditions**

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C



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Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	lcc			450	mA
Data Rate		1.0	10.3	10.7	Gbps

# **Optical and Electrical Characteristics**

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre V	Vavelength	λς	1540	1550	1560	nm	
Spectral W	idth (-20dB)	Δλ			1	nm	
Side-Mode Su	uppression Ratio	SMSR	30	-		dB	
Average C	Output Power	Pout	-1		+4	dBm	1
Extinct	tion Ratio	ER	7.5			dB	
Data Input Sv	wing Differential	V <sub>IN</sub>	180		850	mV	2
Input Differer	ntial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
TV Disable	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
TV Fault	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receiv	er			
Centre V	Vavelength	λς	1540	1550	1560	nm	
Receive	r Sensitivity				-23	dBm	3
Receive	r Overload		-7			dBm	3
LOS	LOS De-Assert				-24	dBm	
LOS	LOS Assert		-35			dBm	
LOS Hysteresis			0.5			dB	
Data Output S	Data Output Swing Differential		300		900	mV	4
1	00	High	2.0		Vcc	V	
L	.OS	Low			0.8	V	

#### Notes:

- The optical power is launched into SMF.
   PECL input, internally AC-coupled and terminated.
   Measured with a PRBS 2<sup>31</sup>-1 test pattern @10312Mbps, BER ≤1×10<sup>-12</sup>.
- 4. Internally AC-coupled.



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# **Timing and Electrical**

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V <sub>H</sub>	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

# **Diagnostics**

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-1 to +4	dBm	±3dB	Internal
RX Power	-23 to -6	dBm	±3dB	Internal

# **Digital Diagnostic Memory Map**

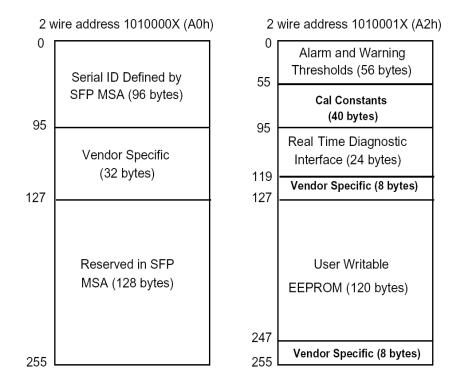
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

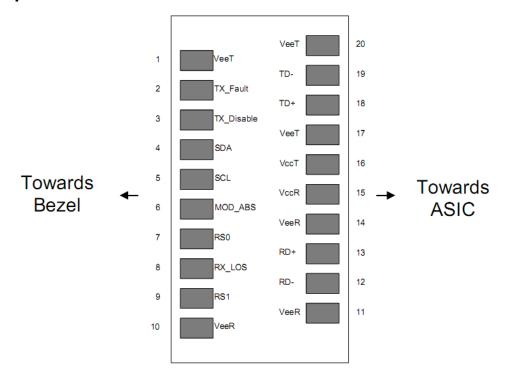


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## The digital diagnostic memory map specific data field defines as following.



## **Pin Descriptions**





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Pin	Signal Name	Description	Plug Seq.	Notes
1	V <sub>EET</sub>	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V <sub>EER</sub>	Receiver ground	1	
11	V <sub>EER</sub>	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V <sub>EER</sub>	Receiver ground	1	
15	V <sub>CCR</sub>	Receiver Power Supply	2	
16	V <sub>CCT</sub>	Transmitter Power Supply	2	
17	V <sub>EET</sub>	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V <sub>EET</sub>	Transmitter Ground	1	

#### Notes:

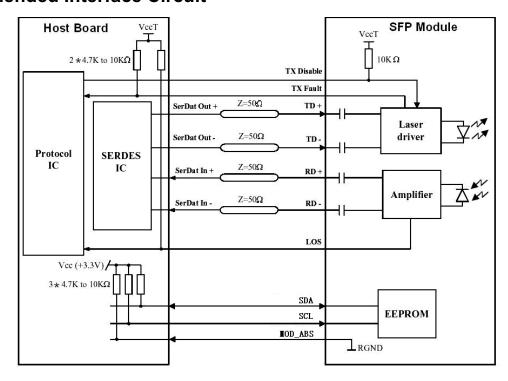
Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with  $100\Omega$  (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

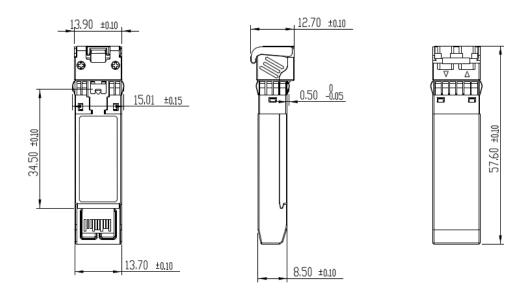


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## **Recommended Interface Circuit**



## **Mechanical Dimensions**







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# **Ordering information**

Part No	Data Rate	Wavelength	Fiber	Reach	Temp	DDM
OP-S10G45-80	10Gbps	Tx1490/Rx1550nm	SMF	80KM	0~70℃	YES
OP-S10G54-80	10Gbps	Tx1550/Rx1490nm	SMF	80KM	0~70℃	YES
OP-S10G45-80E	10Gbps	Tx1490/Rx1550nm	SMF	80KM	-10~80℃	YES
OP-S10G54-80E	10Gbps	Tx1550/Rx1490nm	SMF	80KM	-10~80℃	YES