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### OP-S16GDXX-40

## 14.025Gbps SFP+ DWDM Transceiver, Single Mode, 40km Reach

#### **Product Features**

- Supports up to 14.025Gbps bit rates
- Hot-pluggable SFP+ footprint
- DWDM Cooled EML laser and PIN photodiode, Up to 40km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:
- Standard: 0 to +70°C



## **Applications**

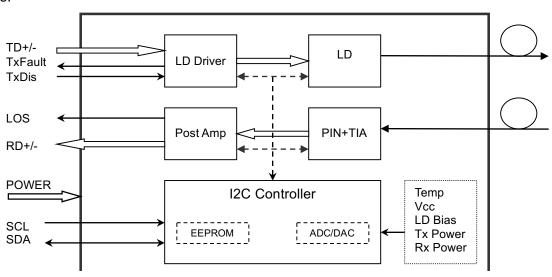
- 4.25/8.5/14.025G Fibre channel
- Other Optical links

## **Description**

The SFP+ transceivers are high performance, cost effective modules supporting data rate of 14.025Gbps and 40km transmission distance with SMF.

The transceiver consists of three sections: a Cooled EML laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.





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# **Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

# **Recommended Operating Conditions**

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			300	mA
Data Rate			14.025		Gbps

# **Optical and Electrical Characteristics**

Parai	meter	Symbol	Min	Typical	Max	Unit	Notes
	Transmitter						
Centre V	Vavelength	λc	λc-0.8	λс	λc+0.8	nm	
Spectral W	idth (-20dB)	Δλ			1	nm	
Side-Mode Su	uppression Ratio	SMSR	30	-		dB	
Average C	Output Power	P <sub>out</sub>	-1		+3	dBm	1
Extinct	tion Ratio	ER	8.2			dB	
Data Input Sv	wing Differential	V <sub>IN</sub>	180		850	mV	2
Input Differer	ntial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
TV Disable	Disable		2.0		Vcc	V	TX Disable
TX Disable	Enable		0		0.8	V	
TV Fault	Fault		2.0		Vcc	V	TX Fault
TX Fault	Normal		0		0.8	V	
Receiver							
Centre V	Vavelength	λς	1260		1620	nm	
Receive	r Sensitivity				-14	dBm	3
Receive	r Overload		0.5			dBm	3



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LOS De-Assert	LOSD		-15	dBm	
LOS Assert	LOSA	-28		dBm	
LOS Hysteresis		0.5		dB	
Data Output Swing Differential	V <sub>out</sub>	300	900	mV	4
100	High	2.0	Vcc	V	
LOS	Low		0.8	V	

#### Notes:

- 1. The optical power is launched into SMF.
- PECL input, internally AC-coupled and terminated.
   Measured with a PRBS 2<sup>31</sup>-1 test pattern @14025Mbps, BER ≤1×10<sup>-12</sup>.
- 4. Internally AC-coupled.

# **Timing and Electrical**

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V <sub>H</sub>	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

# **Diagnostics**

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal



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Bias Current	0 to 15	mA	±10%	Internal
TX Power	-7.8 to -0.5	dBm	±3dB	Internal
RX Power	-16 to -1	dBm	±3dB	Internal

## **Digital Diagnostic Memory Map**

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.

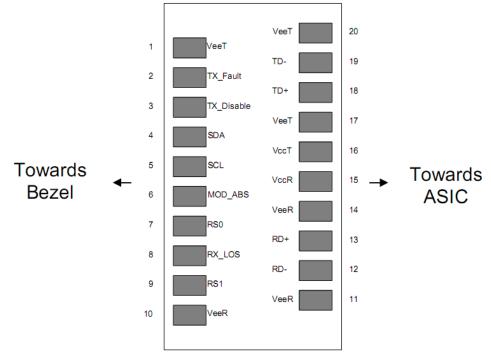
_	vire address 1010000X (A0h	n) 2 1	wire address 1010001X (A2h)
0	Serial ID Defined by	55	Alarm and Warning Thresholds (56 bytes)
95	SFP MSA (96 bytes)	95	Cal Constants (40 bytes)
95	Vendor Specific (32 bytes) 11		Real Time Diagnostic Interface (24 bytes)
127		119 127	Vendor Specific (8 bytes)
	Reserved in SFP MSA (128 bytes)		User Writable EEPROM (120 bytes)
255		247 255	Vendor Specific (8 bytes)

	wire address 1010001X (A2h
0 55	Alarm and Warning Thresholds (56 bytes)
95	Cal Constants (40 bytes)
	Real Time Diagnostic Interface (24 bytes)
<ul><li>119</li><li>127</li></ul>	Vendor Specific (8 bytes)
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# **Pin Descriptions**



Pin	Signal Name	Description	Plug Seq.	Notes
1	V <sub>EET</sub>	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V <sub>EER</sub>	Receiver ground	1	
11	V <sub>EER</sub>	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V <sub>EER</sub>	Receiver ground	1	
15	V <sub>CCR</sub>	Receiver Power Supply	2	
16	V <sub>CCT</sub>	Transmitter Power Supply	2	
17	V <sub>EET</sub>	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5



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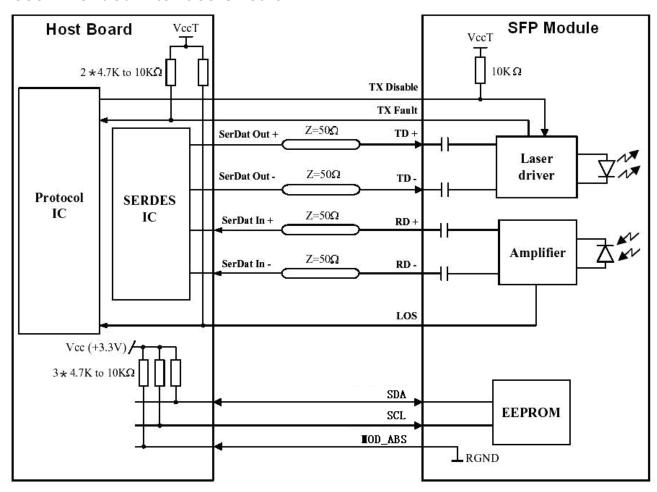
19	TD-	Inv. Transmit Data In	3	Note 5
20	$V_{EET}$	Transmitter Ground	1	

#### Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with  $100\Omega$  (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

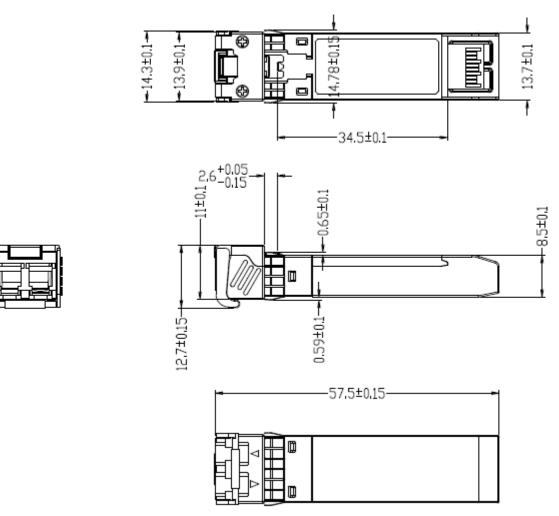
## **Recommended Interface Circuit**





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## **Mechanical Dimensions**



# **Ordering information**

Part Number	Product Description
OP-S16GDXX-40	14.025Gbps DWDM Channel17-61 LC 40km 0°C~+70°C with DDM